



Device	Voh	Vol	Vih	Vil	Voh	Vol	Vih	Vil	Margin
BF533	2.4	0.4	2.0	0.6	2.0	0.8	2.8	0.2	0.4
74LV244	Vih	Vil	Voh	Vol	2.0	0.8	2.8	0.2	0.4
Si3050	2.4	0.35	2.0	0.8	2.0	0.8	2.8	0.2	0.4
74LV244	Vih	Vil	Voh	Vol	2.0	0.8	2.8	0.2	0.4
Si3050	2.4	0.35	2.0	0.8	2.0	0.6	2.4	0.4	0.4
BF533	Vih	Vil	Voh	Vol	2.0	0.6	2.4	0.4	0.4

U2 AND BUFFERING

U2 allows us to isolate the very fast BF533 signals from our circuit. The banks of Rs on either side of U2 allow independent adjustment of rise times.

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FOUR PORT FXO/FXS MODULE DAUGHTER CARD FOR BF533 STAMP

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Experiment with grounding points to minimise EMI. Optionally DNL R22/R23